


Name of Teaching Staff:		Dr. Kshirsagar Dhananjay Bhanudas			
Designation:		Professor			
Department:		Electronics and Computer Engineering			
Date of joining	20/12/1993	Date of Retirement	31/07/2031		
Date of Birth	25/07/1971	Type of association	Permanent/Adjunct/Visiting		
UniqueID:					
E-MailID:		hodcompcoe@sanjivani.org.in			
Qualification with Class/Grade:		U.G.	P.G.	PhD	
		B.E.(Computer Engineering) (Distinction)	M.E.(CSE) (Distinction)	PhD(CSE)	
Total experience in years:		Teaching: 32 years	Research: 5	Industry: -	Others:
Area of Specialization		Image Processing, ANN, Machine Learning, Computer Architectures			
Courses Taught		Computer Organization and Architecture, Microprocessors, ANN, Advanced Computer Architecture, C Programming, Project Management			
Papers published:		National:04 International:28			
Papers presented in conferences:		National:02 International:02			
PhD Guide? Give field & University:		Nil			
PhDs/Projects Guided:		PhDs: Nil Projects PG :28 UG:48			
Project carried out		2			
Patents(Filed/Granted):		Nil			
Technology Transfer		-			
No of Books published with details		Name of the Book:	Publisher:	ISBN:	Year:
		-			
Professional Memberships:		LMISTE, MIEEE, MIE			
Consultancy Activities:		Nil			
Awards:		Outstanding Engineer Award by Institution of Engineers(India), Ahmednagar Zone			
Grants fetched:		BCUD, MODROB			
Interaction with professional institutions:		Worked as Resource person for FDP and expert lectures, Reviewer & Session Chair, BOS Member SPPU University			