# SRES', Sanjivani College of Engineering, Kopargaon (An Autonomous Institute affiliated to SPPU, Pune)

# Staff Profile

# **Personal Information**

Name of Teaching Staff: Sayyad Muzffarali Akbar

**Designation: Assistant Professor** 

**Department: Electronics and Computer Engineering** 

**Date of joining: 15/01/2001** 



E Mail ID:	sayyadmaece@sanjivani.org.in
Contact No:	9421805884

#### **Education Details**

Qualification	Specialization with Class or Grade	University	Year	
UG	First	First NMU Jalgaon		
PG	First	Shivaji University Kolhapur	2010	
Ph.D	Electronics and	ectronics and Savitribai Phule Pune		
	Telecommunication	Telecommunication University, Pune		
	Engineering			
Total experience in years:				
Teaching:	24 Years			

# 24 Years

7 Months **Industry:** 

### **Research and Publications**

Author(s), Title of Article, Name of Journal, Volume (Issue), ISSN: xxxx-xxxx, pp:xx, Month-Year [ Download Link]

Sayyad M. A, Agarkar B. S. (2023) Modified Architecture for Nikhilam Navatshcaramam Dashath (NND) Vedic Multiplier. Indian Journal of Science and Technology. 16(42): 3727-3734. https://doi.org/10.17485/IJST/v16i42.733

M.A. Sayyad, B.S. Agarkar. Comparative Analysis of Partial Product Addition in Vedic Urdhva Tiryakbhyam Multiplier. Journal of VLSI Design Tools & Technology. 2023; 13(1): 15-32p,

link:https://engineeringjournals.stmjournals.in/index.php/JoVDTT/article/view/7325

M. A. Sayyad, D.G.Lokhande, P.M.Vibhute, "Artificial Intelligence Techniques in Automated Vehicle Systems: A Comprehensive Review", Advances in Nonlinear Variational Inequalities, Vol. 27 (3), pp. 524-540, 2024,

https://doi.org/10.52783/anvi.v27.1417

## Papers presented in conferences:

M. A. Sayyad, D. N. Kyatanavar, "A Novel Method of Multiplication with EkanyunenaPurvena" Advances in VLSI and Embedded Systems, Publisher: Springer Singapore

Print ISBN: 978-981-15-6228-0 ,Electronic ISBN: 978-981-15-6229-7

M. A. Sayyad and D. N. Kyatanavar, "Optimization for Addition of Partial Product in Vedic Multiplier," 2017 International Conference on Computing, Communication, Control and Automation (ICCUBEA), Pune, India, 2017, pp. 1-4, doi: 10.1109/ICCUBEA.2017.8463787.

P. R. Tupe, P. M. Vibhute and M. A. Sayyad, "An Architecture Combining Convolutional Neural Network (CNN) with Batch Normalization for Apparel Image Classification," 2020 IEEE International Symposium on Sustainable Energy, Signal Processing and Cyber Security (iSSSC), 2020, pp. 1-6, doi: 10.1109/iSSSC50941.2020.9358882.

# Research Profile URL Sayyad Muzffarali - Google Scholarhttps://scholar.google.com/citations?hl=en&user=ZOJ2hJQA AAAJ&view op=list works&gmla=AJsN-**Google Scholar** F4Unp0q367 wOLxlkJEdwW4iq6ygRiLhV--UVKfWHFxhRZLDYGD7DLu9HT52Zugh9HnTtSa-ZMPBoW6fL10VK1TJ43XN6pn4QH1UGZgQ2ocuW3V1 I Scopus preview - Sayyad, M. A. - Author details -**Scopus** Scopushttps://www.scopus.com/authid/detail.uri?authorld=572223 54337 Orcid **Other Research Details** Ph D Guide ?Give field & **University:** Ph Ds/ Projects Guided: **Books Published IPRs/Patents Professional Memberships:**

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		Consultancy	Activities		
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OTHER Professional URLs		
Personal Website		
Linked In Profile	https://www.linkedin.com/in/sayyad-m-3343076/	
GitHub URL	https://github.com/sayyadma/	
YouTube Channel	https://www.youtube.com/channel/UCOBqSWjRHzoG6gw_RRu yDpg	
Other		

Any Other: